

SBR3U100LP

3A SBR[®] SUPER BARRIER RECTIFER

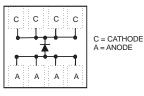
Features

- Ultra Low Forward Voltage Drop
- Superior Reverse Avalanche Capability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound Device (Note 2)

Mechanical Data

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- Case: DFN3030-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish NiPdAu annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208 🕄
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.0172 grams (approximate)



Bottom View

Device Schematic

Maximum Ratings @T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vrm	100	V
RMS Reverse Voltage	V _{R(RMS)}	70	V
Average Rectified Output Current	lo	3.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	32	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance Thermal Resistance Junction to Ambient (Note 3) $T_A = 25^{\circ}C$	$R_{ heta JA}$	61	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 4)	V _{(BR)R}	100	-	-	V	I _R = 0.2mA
Forward Voltage	V _F	-	-	0.79	V	I _F = 3.0A, T _J = 25°C
Reverse Current (Note 4)	I _R	-	16 3	200 15	μA mA	$V_R = 100V, T_J = 25^{\circ}C$ $V_R = 100V, T_J = 125^{\circ}C$

Notes:

1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.

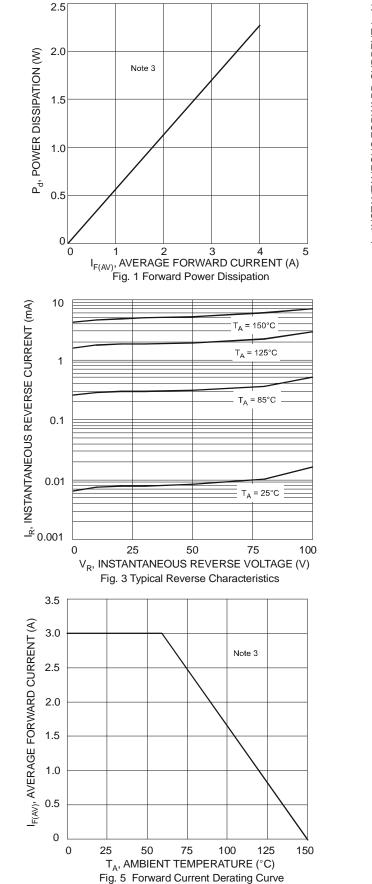
2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php

3. Device mounted on Polyimide substrate, 2 oz. Copper, 75mm² pad area, double side PCB.

4. Short duration pulse test used to minimize self-heating effect.







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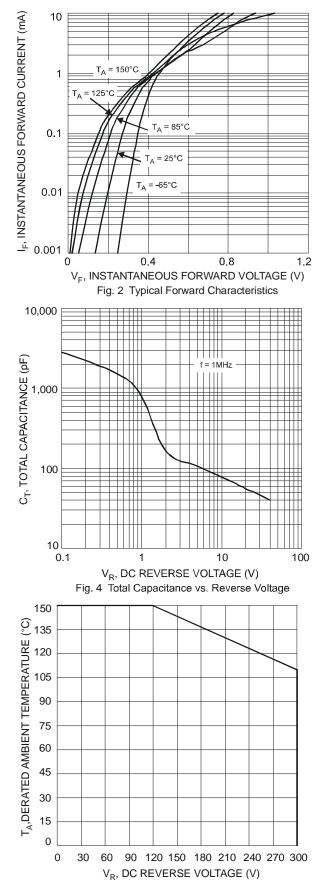


Fig. 6 Operating Temperature Derating



Ordering Information (Note 6)

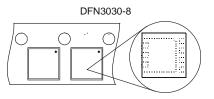
Part Number	Case	Packaging
SBR3U100LP-7	DFN3030-8	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

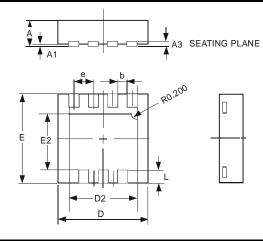
Marking Information



3U10 = Product marking code YYWW = Date code marking YY = Last digit of year ex: 06 for 2006 WW = Week code 01 to 52

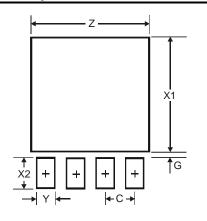


Package Outline Dimensions



DFN3030-8				
Dim	Min	Max	Тур	
Α	0.57	0.63	0.60	
A1	0	0.05	0.02	
A3			0.15	
b	0.29	0.39	0.34	
D	2.90	3.10	3.00	
D2	2.19	2.39	2.29	
е			0.65	
Е	2.90	3.10	3.00	
E2	1.64	1.84	1.74	
L	0.30	0.60	0.45	
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.59
G	0.11
X1	2.49
X2	0.65
Y	0.39
С	0.65

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